

FIG.-2

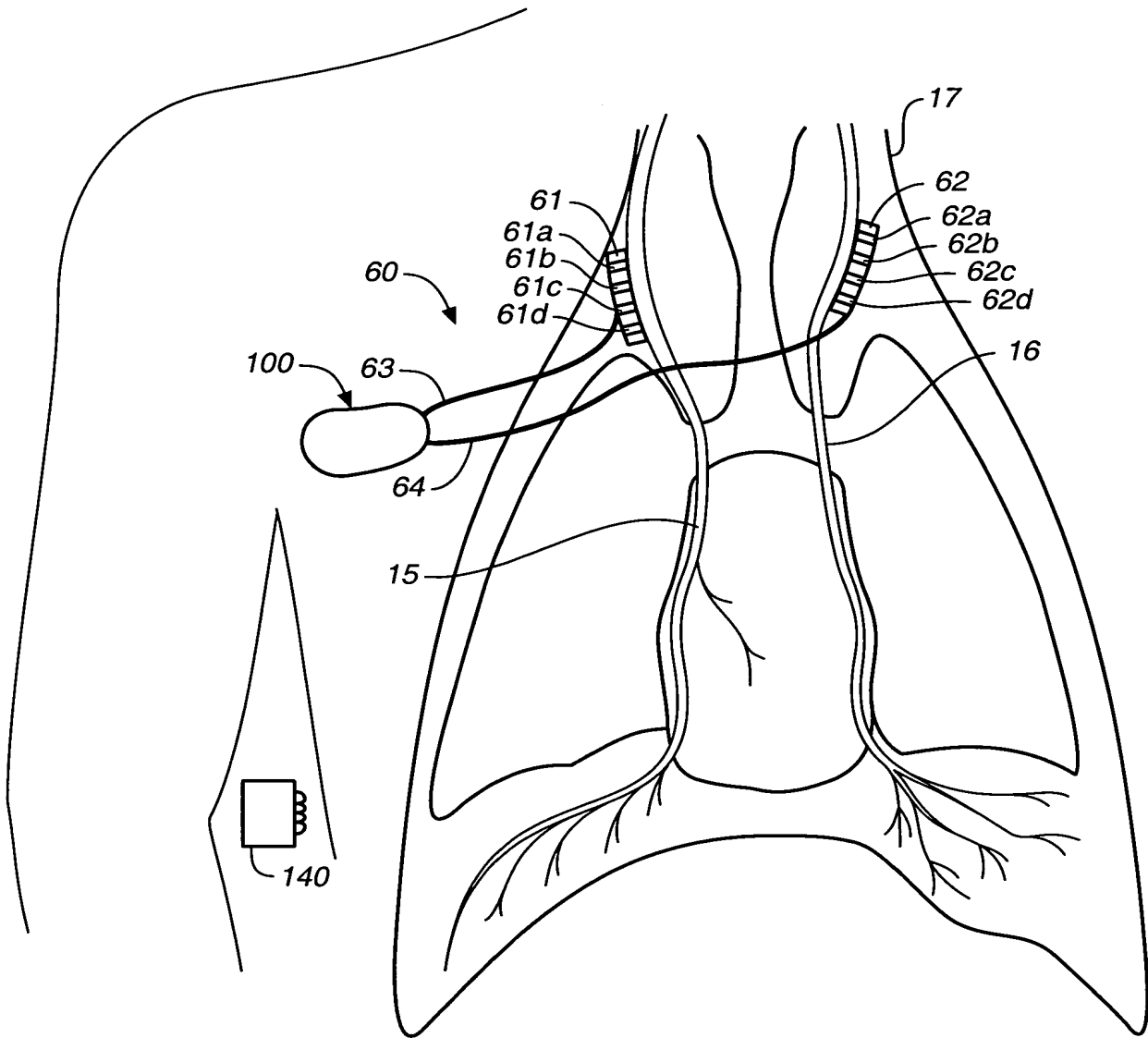


FIG._3

FIG._4

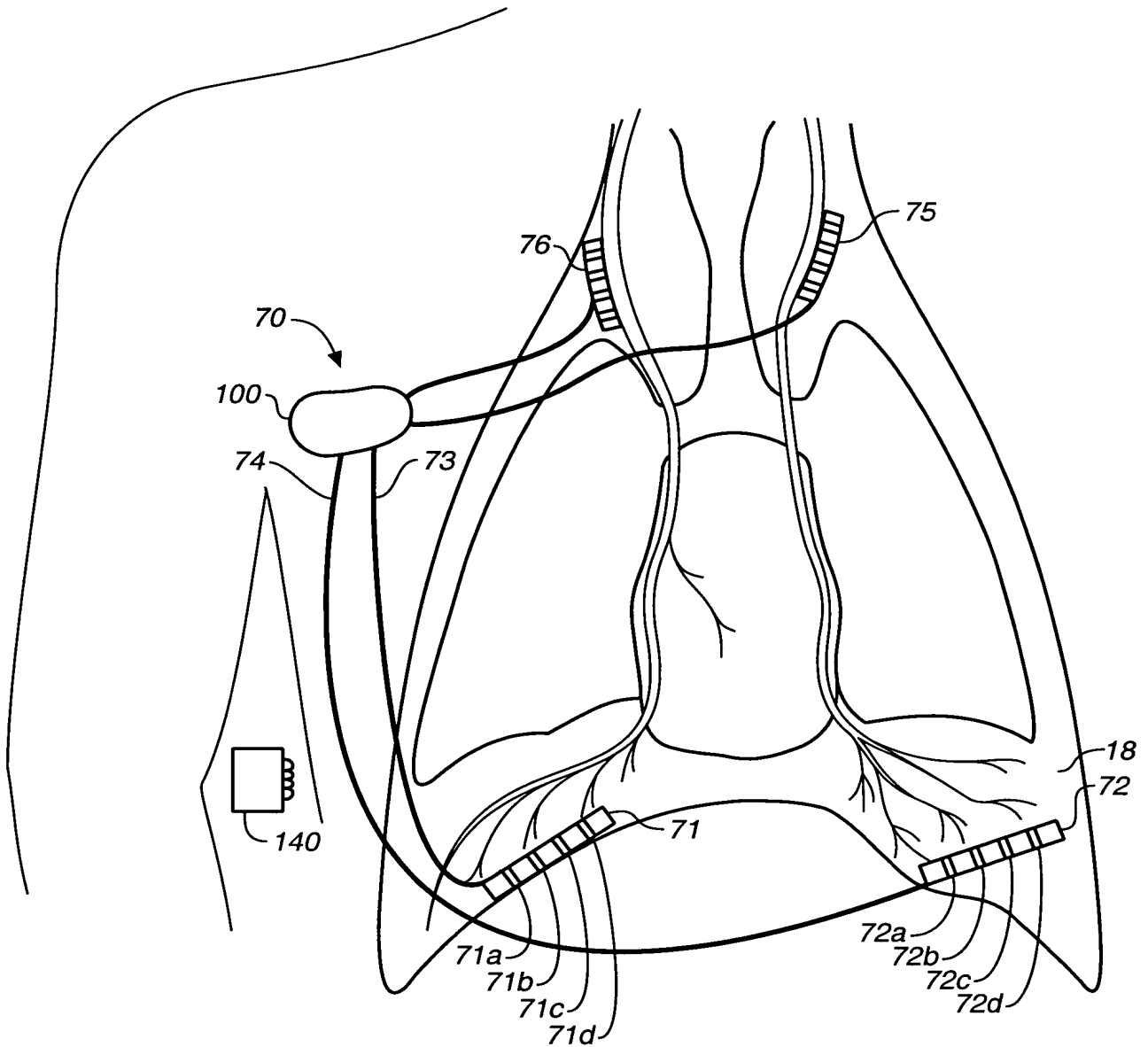


FIG._5

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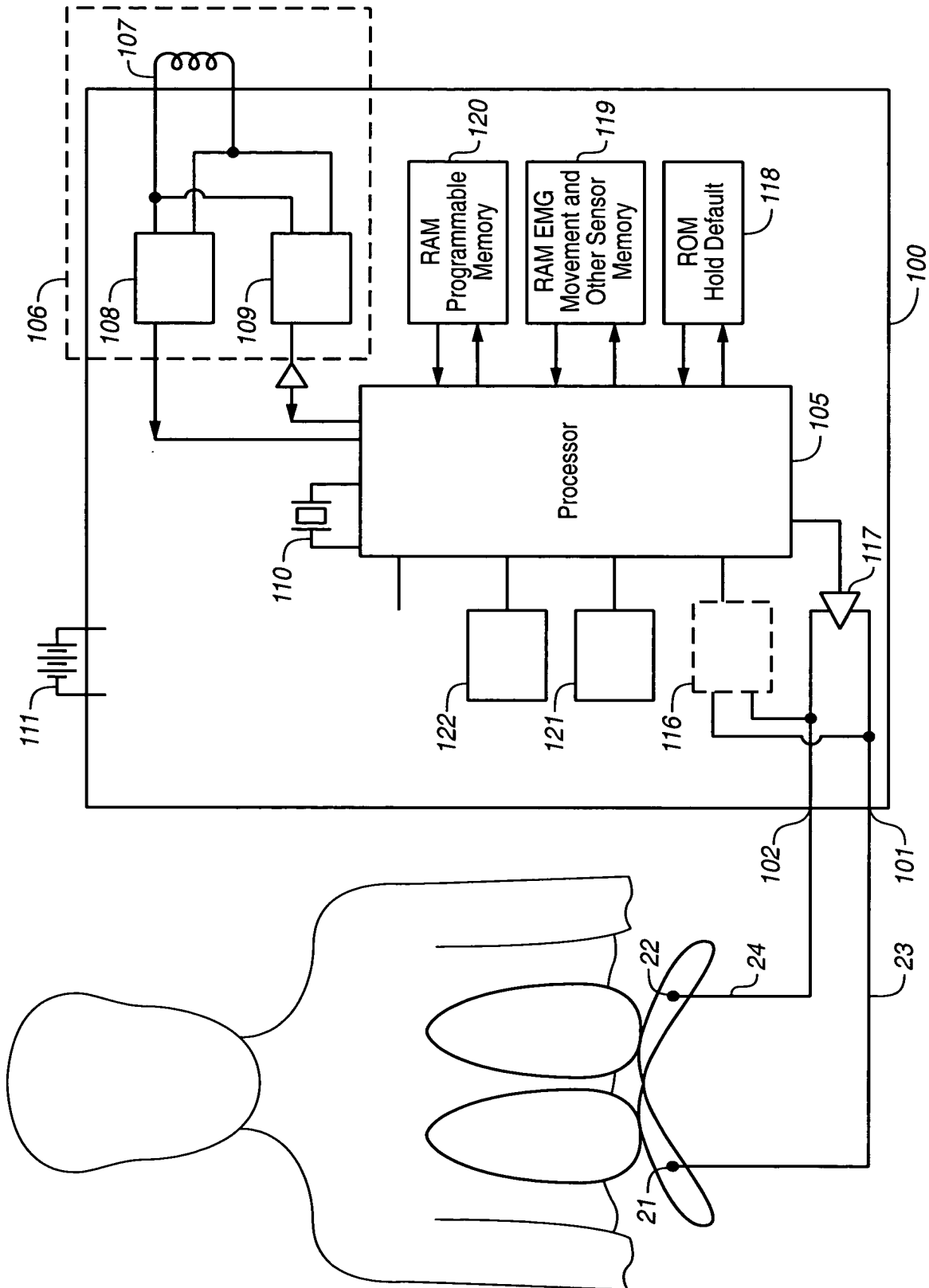


FIG. 6

FIG. 7A

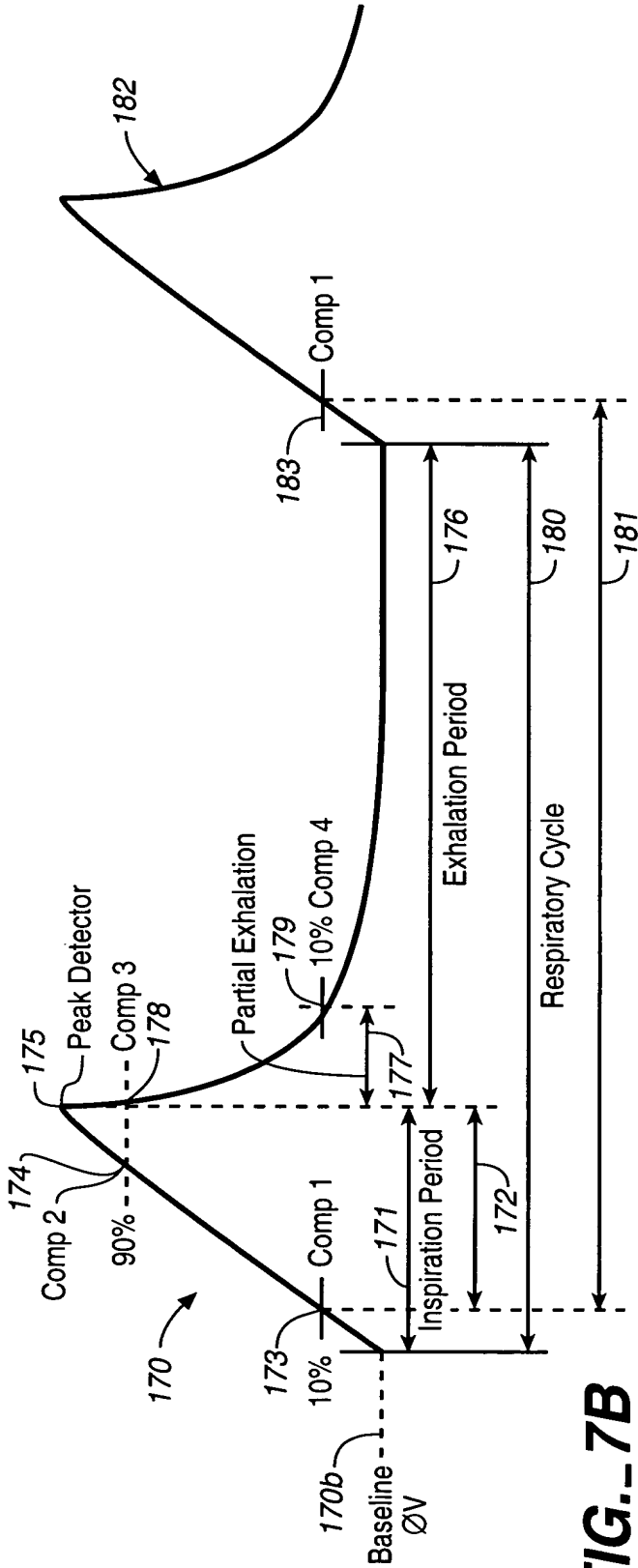
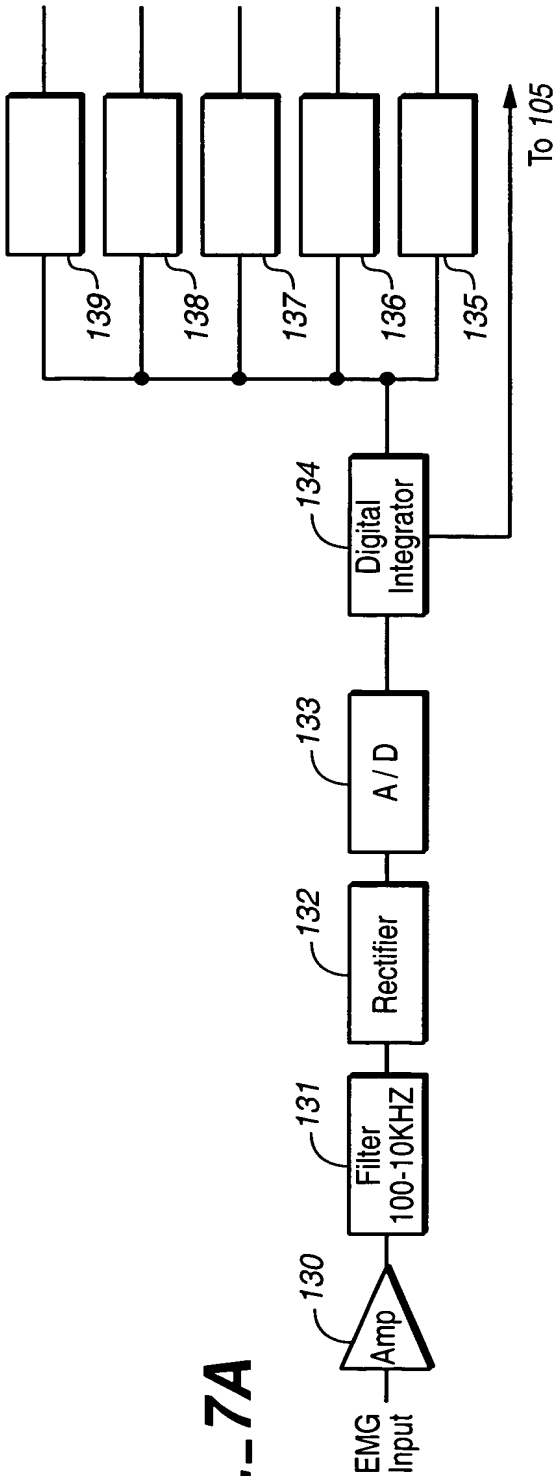


FIG. 7B

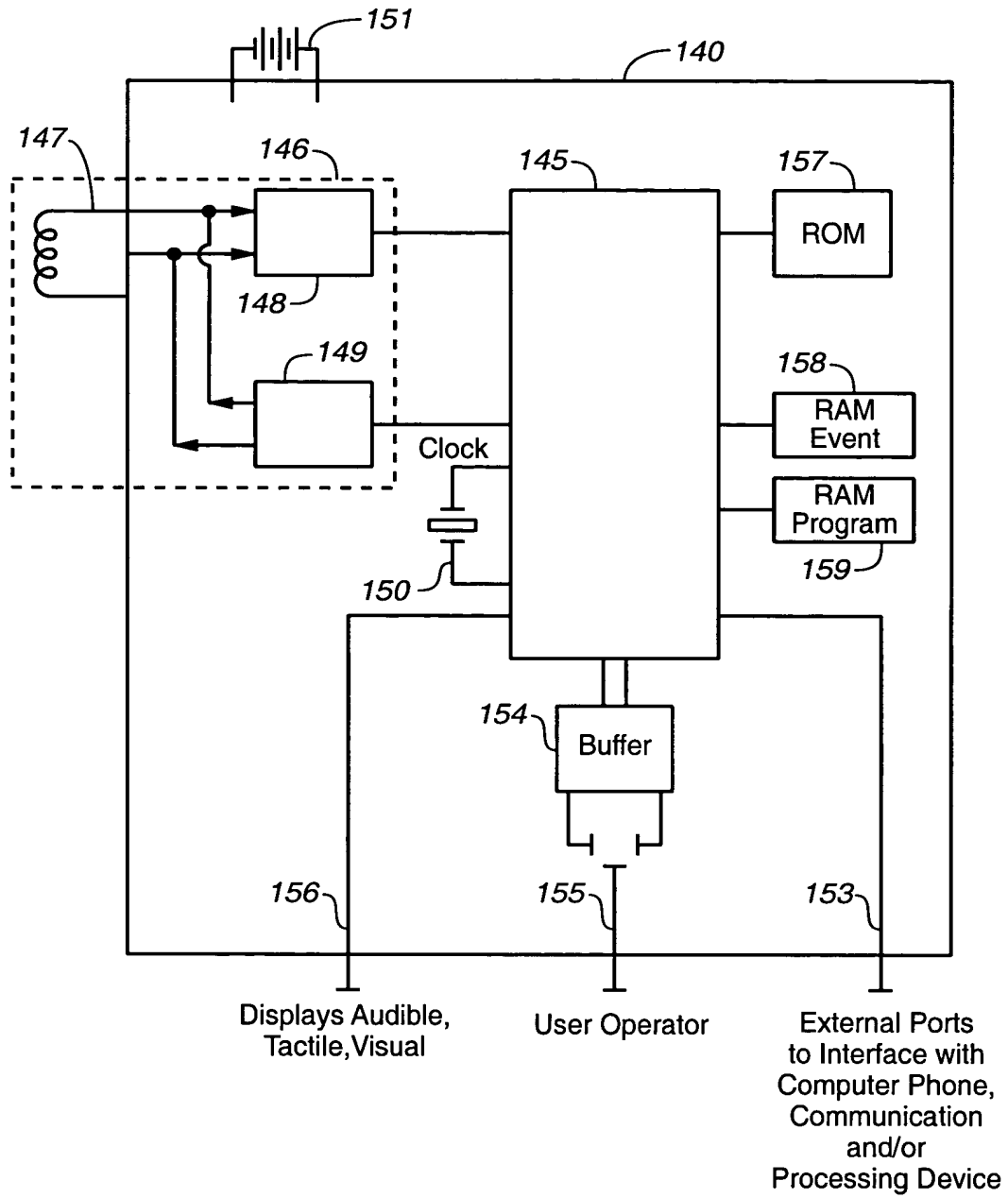
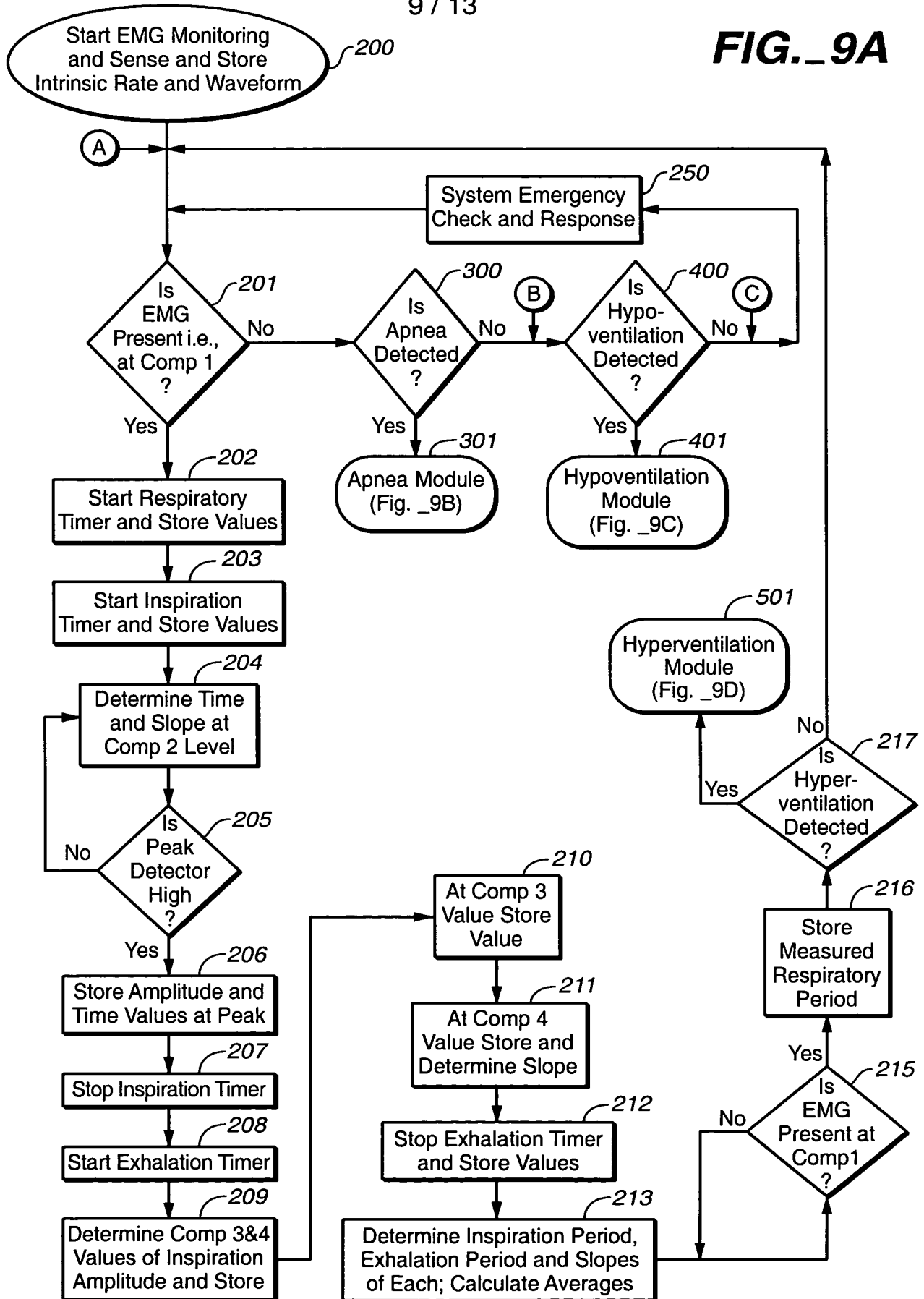
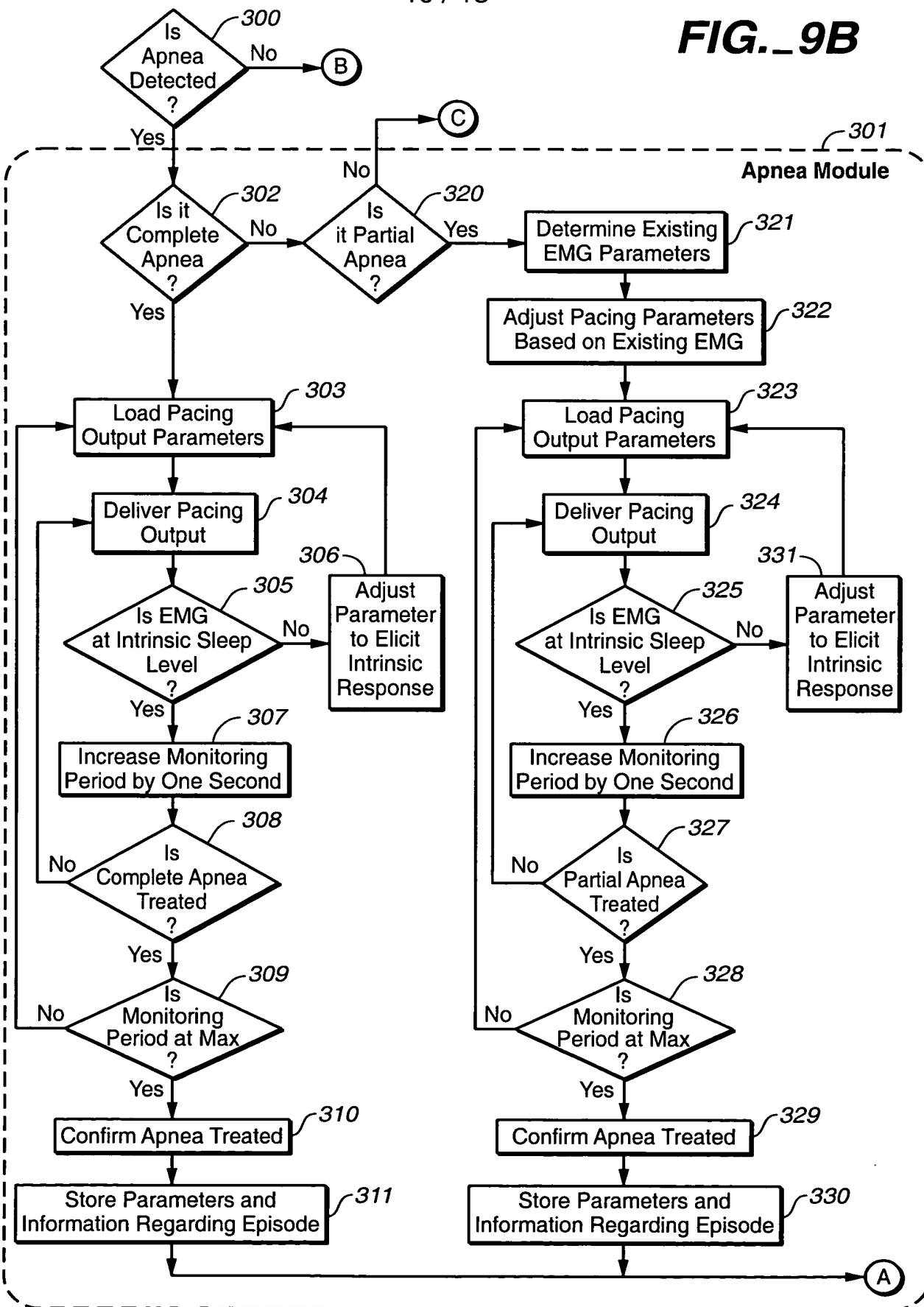


FIG._8

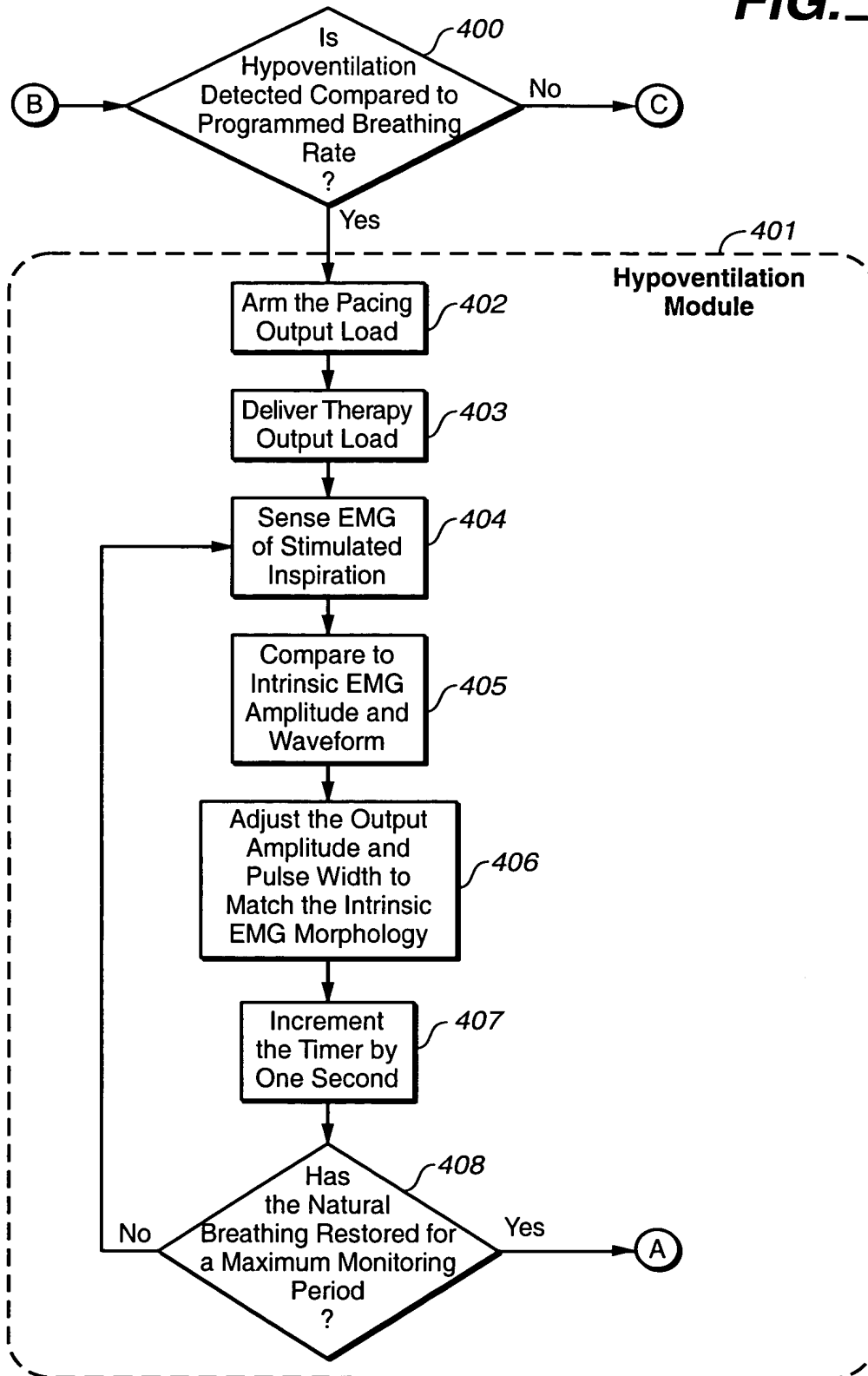
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FIG. 9A

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FIG. 9B

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FIG. 9C

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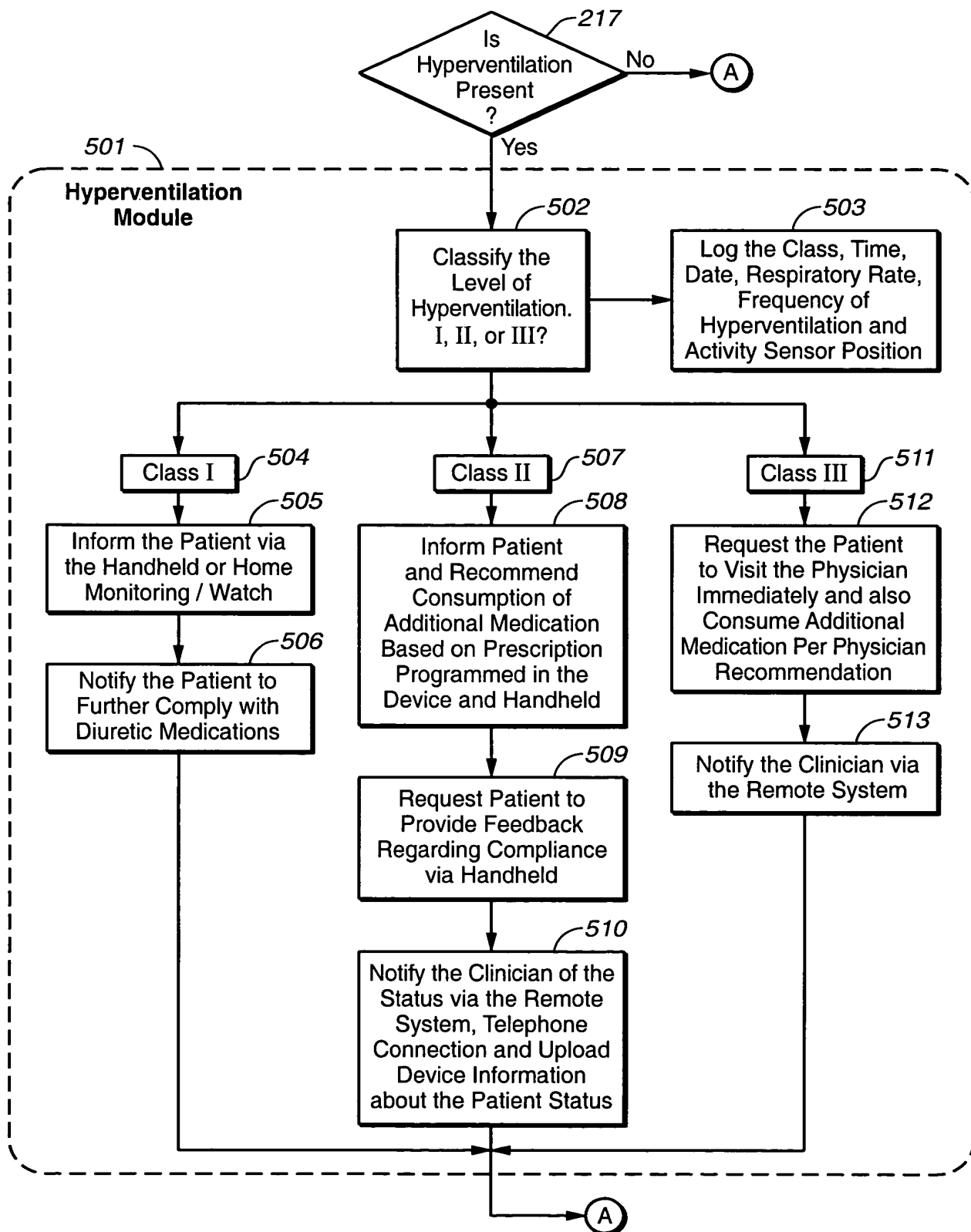
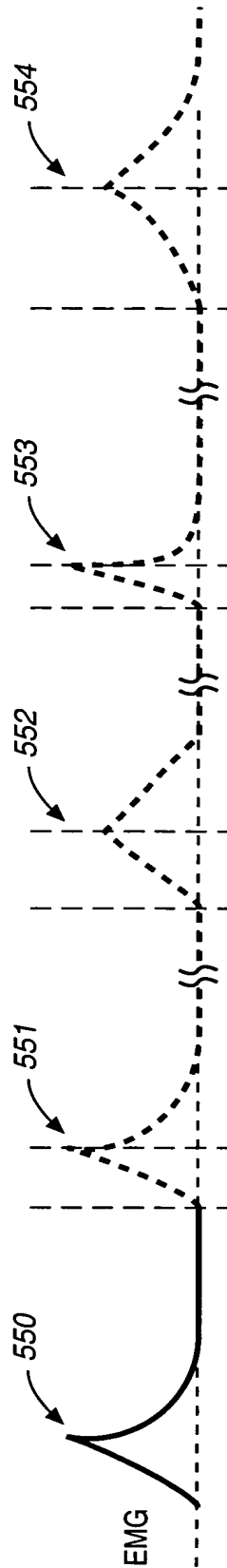
FIG. 9D

FIG. 10A



Voltage / Current

Pacing
Output

FIG. 10B

